IN THE UNITED STATES PATENT AND TRADEMARK O

0 2000

Peter Warnes, et al.

09/523,877

March 13, 2000

For

METHOD AND APPARATUS FOR JUMP

DELAY SLOT CONTROL IN A PIPELINED

PROCESSOR

Examiner

Unknown

Art Unit

Unknown

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, Attn: Application Processing Division, Customer Correction Branch, on

> June 28, 2000 (Date)

> > Robert F. Gazdzinski

Reg. No. 39,990

REQUEST FOR CORRECTION OF FILING RECEIPT

Assistant Commissioner for Patents Washington, D.C. 20231

ATTN:

APPLICATION PROCESSING DIVISION

CUSTOMER CORRECTION BRANCH

Dear Sir:

Applicant hereby respectfully requests that the official Filing Receipt be corrected as follows:

The Continuing Data as Claimed by Applicant should read:

THIS APPLN CLAIMS BENEFIT OF 60/134,253 FILED 05/13/1999 AND 60/104,271 FILED 10/14/1998

A copy of the Filing Receipt with the correction indicated in red ink is enclosed. Please forward the corrected Filing Receipt to the undersigned.

Respectfully submitted,

GAZDZINSKI & ASSOCIATES

Dated: June 28, 2000

Robert F. Gazdzinski

Registration No. 39,990

Attorney of Record

3636 Nobel Drive, Suite 215

San Diego, CA 92122

Telephone No. (858) 320-2031

Facsimile No. (858) 320-2034





UNITED STATES DEPARTMENT OF COMMERCE Patent and Trademark Office

Address: ASSISTANT SECRETARY AND

COMMISSIONER OF PATENT AND TRADEMARKS

Washington, D.C. 20231

				•	•	\sim	
APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	CLAIMS	R CLAMPS
09/523,877	03/13/2000	2784	0	ARC.005A	9	24 MAIL	EIVE
Robert F Gazdzinski Gazdzinski & Associate 3636 Nobel Drive Suite San Diego, CA 92122						ROUM	

Date Mailed: 05/15/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Peter Warnes, London, UNITED KINGDOM; Carl Graham, London, UNITED KINGDOM;

Continuing Data as Claimed by Applicant

FILED

THIS APPLN CLAIMS BENEFIT OF 60/134,253 05/13/1999
AND 60/104,271 FIVED 10/14/1998

Foreign Applications

If Required, Foreign Filing License Granted 05/12/2000

Title

Method and apparatus for jump delay slot control in a pipelined processor

Preliminary Class

712

Data entry by : BURNS, ERIC

Team: OIPE

Date: 05/15/2000











Bib Data Sheet

1 of 1



UNITED STATES DEPARTMENT OF COMMERCE Patent and Trademark Office

Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231

SERIAL NUMBEI 09/523,877	R 03/13/2000 RULE _	CLASS 712	GROUP ART 2784	UNIT	ATTORNEY DOCKET NO. ARC.005A					
APPLICANTS Peter Warnes, London, UNITED KINGDOM; Carl Graham, London, UNITED KINGDOM;										
** CONTINUING DATA **********************************										
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/12/2000 —										
Foreign Priority claimed 35 USC 119 (a-d) condit met Verified and Acknowledged	Allowance	state or country UNITED KINGDOM	SHEETS DRAWING 9	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 9					
ADDRESS Robert F Gazdzinsk Gazdzinski & Assoc 3636 Nobel Drive S San Diego ,CA 921	ciates uite 215	-								
TITLE										
Method and apparatus for jump delay slot control in a pipelined processor										
RECEIVED No	EES: Authority has been gi b to charge/cro b for following	edit DEPOSIT ACCOU	1.16 1.17 time)	All Fees 1.16 Fees (Filing) 1.17 Fees (Processing Ext. of time) 1.18 Fees (Issue)						
				Other						